Amendments to the Specification:

Please replace the paragraph starting on page numbered 6, line 6 with the following paragraph:

Figure 1 illustrates a block diagram of a digital video encoder with which some

embodiments are implemented.

Please replace the paragraph starting on page numbered 8, line 14 with the following

paragraph:

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Figure 1 illustrates a high level block diagram of a typical digital video encoder 100 upon

which some embodiments are implemented as is well known in the art. The digital video

encoder 100 receives incoming video stream 105 of video frames at the left of the block diagram.

Each video frame is processed by a Discrete Cosine Transformation (DCT) unit 110. The video

frame may be processed independently (an intra-frame) or with reference to information from

other video frames received from the motion compensation unit (an inter-frame). A Quantizer

(Q) unit 120 then quantizes the information from the Discrete Cosine Transformation unit 110.

2

The quantized frame information is then encoded with an entropy encoder (H) unit 180 to

produce an encoded video bitstream.

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